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ECE 5780

Postlab 06

1. If the DAC is updated every 4us with 200-elements making up one cycle of a waveform, then the frequency of the output wave is:

250 kHz / 200 samples per cycle = 1.25 kHz

1. If the ADC is in 12-bit mode with 4096 steps from 0V to 3V, then:

The resolution is about 0.73 mV per step.

The ADC output value is 2389 if the input voltage is 1.75V.